

Associate Professor

Graduate School of Engineering, Nagoya Institute of Technology, JAPAN

1. Position Associate Professor

2. Duties

1) Teaching electronic circuits, electrical circuits, electromagnetics and related subjects
All the faculty members are responsible for general education classes. Therefore, please be aware that you may be assigned to these subjects.

2) Supervising undergraduate and graduate research

3) Research of high reliability communication, biological communication, energy wireless transmission and wide area of wireless technology in electronic and electrical engineering

3. Qualifications

1) Holder of a doctoral degree

2) Enthusiasm for teaching electronic circuits, electrical circuits, and electromagnetics

3) Experience in the research and development of wireless technologies

4) An adequate command of Japanese to manage daily university works

4. Salary and Benefits will be determined according to the university regulations.

Annual salary system will be applied.

5. Starting Date: February 1, 2027 or the earliest possible date thereafter

6. Application Materials

1) A full statement of Curriculum vitae with a complete list of academic achievements and a picture of the applicant

2) Copies of three major publications

3) Status of acquisition of external funds (For items such as Grants-in-Aid for Scientific Research, commissioned research funds, joint research funds, scholarship donations, research grants, etc., please provide the name of the funds, the year, the research theme, the amount, and whether the applicant is the representative or a member of the research team, etc.)

4) A concise description of past research and future goals: approximately 500 words.

5) An essay outlining your philosophy of teaching: approximately 500 words.

6) Contact information of two references (Names, titles, your relationship and contact addresses of two people from whom remarks can be acquired)

7. Application Deadline: No later than August 31, 2026

8. Selection: After the document screening, a presentation and interview may be conducted if necessary (travel expenses to be borne by the applicant).

9. Notification of Result: We will notify you of the result by email within 10 days after the interview.

10. Send the Application Materials via JREC-IN Portal Web System:

JREC-IN: D126051271

<https://jrecin.jst.go.jp/seek/SeekJorDetail/Changelang?id=D126051271&lang=1>

Notes

- 1) Application materials in English or in Japanese are acceptable.
- 2) Submission via JREC-IN Portal Web System
Please convert your application documents into PDF files, and either compress all the documents into a single ZIP file or combine them into a single PDF. Applications will be accepted via the JREC-IN Portal Web system. We do not accept applications via email.
- 3) NITech promotes diversity, including gender equality, in its effort to enhance the university environment for education, research and employment. In the faculty recruitment selection, if the overall performance (research achievements, education performance, social contribution, etc.) and personal evaluation are recognized as equivalent to those of other candidates, we actively seek to hire diverse candidates regardless of gender or nationality.
- 4) The personal information related to the screening will be treated properly based on the Personal Information Protection Law and Nagoya Institute of Technology's provision. However, in the screening process, personal information would be inquired. Upon agreement and acceptance of the requirements, please apply for this position.
- 5) For further information, please contact Prof. Kunio SAKAKIBARA by e-mail.
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