"Technology Considerations for Next-Generation Computer Architecture"

Professor: Jean-Luc Gaudiot

August 22th (Mon), 2016

13:00 - 14:30

Main Hall, Bldg.4

Research Keyword: Computer Architecture

NITECH FRONTIER RESEARCH INSTITUTES

Jean-Luc Gaudiot, a professor of electrical engineering and computer science at the University of California, Irvine, has served the Computer Society for many years in a variety of offices, which has given him experience in many activities of the Society. He currently serves as a Vice President for Publications, and Board of Governors. He was 2013 Vice President for Educational Activities. He was a founder and the first editor in chief of IEEE Computer Architecture Letters and of Transactions on Computers, chair of the IEEE Computer Society Technical Committee on Computer Architecture for two terms, and program committee and general chair of many major conferences. Gaudiot is a Computer Society Golden Core member and a Fellow of the IEEE and the AAAS. Before joining UCI in 2002, where he was department chair for six years, Gaudiot was a professor of electrical engineering at the University of Southern California. His industrial experience includes software engineering at Teledyne Controls and design of innovative processor architectures at TRW. Gaudiot has more than 250 refereed publications in the field of computer architecture. He has a PhD in computer science from the University of California, Los Angeles.

Career
2002-present   Professor, Department of Electrical Engineering and Computer Science,
*   University of California, Irvine, USA*
1996-2001   Professor, Department of Electrical Engineering Systems, University of Southern
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1982   PhD in Computer Science University of California, Los Angels, USA
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